



**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:**

Fee et al.

**Serial No.:** 10/729,180

**Filed:** December 5, 2003

**For:** QUAD FLAT NO LEAD (QFN) GRID  
ARRAY PACKAGE, METHOD OF  
MAKING AND MEMORY MODULE AND  
COMPUTER SYSTEM INCLUDING SAME

**Confirmation No.:** 8592

**Examiner:** T. Le

**Group Art Unit:** 2827

**Attorney Docket No.:** 2269-4738.2US  
(00-1113.02/US)

**Notice of Allowance Mailed:**

January 10, 2006

**NOTICE OF EXPRESS MAILING**

Express Mail Mailing Label Number: EL995989967US

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**AMENDMENT PURSUANT TO 37 C.F.R. § 1.312(a)**

Mail Stop ISSUE FEE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

Please amend the above-referenced application as follows: